Semiconductor Engineering 2

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3rd Week 4th Week	MOSFET, CMOS		Can explain how MOS devices work Can explain how ICs and LSs are constructed and	V-C 4 62
4th Week	Integrated circuits (Ice) and t	heir applications	Can explain how ICs and LSIs are constructed and	V-C 4 62
		heir applications	Can explain how ICs and LSIs are constructed and used in electrical and electronic circuits	
5th Week	Power transistors			
			Can explain how semiconductor devices are used in power control	
6th Week	National Holiday			
7th Week	Development and design of l	.9	Can explain how LSIs are developed and designed	
8th Week	Preparing for Mid-term exam	ination	Review problems for the mid term examination,	-
9th Week	National Holiday			
10th Week	Mid-term examination		Can slove problems at the mid-term examination.	
11th Week	Return exam papers and fee technology of silicon wafers	Return exam papers and feedback / Production technology of silicon wafers		
12th Week	LSI production process (1) chips		Can explain the outline of LSI chips' production process,	
13th Week	LSI production process (2) paokages		Can explain the outline of LSI packages' production process,	
14th Week	Heterojunction semiconductor devices		Can explain how heterojunction semiconductor devices work.	
15th Week	Optical and thermo-electrical semiconductor devices	Optical and thermo-electrical properties of semiconductor devices		V-C 3 6
16th Week	Electromagnetic, and quantu semiconductor devices	Electromagnetic, and quantum effect properties of		V-C 3 6
17th Week	National Holiday			
18th Week	Preparing for final examination	Preparing for final examination		
19th Week				
		Final Examination		
20th Week	Return exam papers and fee	dbaok	Review and summarize the learning.	Dor
asic Ability	Examination 60	Quiz 40	Mutual Evoluations between students	Report Particle Othe