Lab work 2 for Introduction Engineering

Basic Course Information			
Course Number	02005129	Subject Category	Compulsory (C)
Class Format	Experiment / Practical training	Credit Type and Number of Credits	1
Department	Computer	Student Category	Year 1
Period of Study	Semester 2	Classes per Week	3
Required Materials	To be announced		_

Course Objective
This course overs the fundamentals of computer engineering list work topics such as VM. command line, and digital
crout, and notubles coding learning with a block-based coding approach and object-vierined coding with Python. The
Students are provided a strong foundation in late yout distin sealed to formalists without writing the engineering problems.
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Evaluation(Rubrio)	Ideal Level of Achievement (Very Good)	Standard Level of Achievement (Good)	Unacceptable Level of Achievement (Fail)
	Ideal Level of Achievement (Very Good)	Standard Level of Achievement (Good)	Unacceptable Level of Achievement (Fail)
Following and Doing Procedure	Demonstrates very good knowledge of the lab procedures and principles	Demonstrates good knowledge of the lab procedures and principle	Lacks the appropriate knowledge of the lab procedures and principles
Data Collection	Measurements are both accurate and precise	Measurements are mostly accurate and precise	Measurements are incomplete, inaccurate and imprecise
Report writing	Content is comprehensive, and accurate, important points are stated clearly with supported data.	Some contents are not comprehensive or incomplete, important points are addressed, but not well supported.	Most of the content is incomplete, important points are addressed and /or inconsistent.
Safety	Proper safety precautions and awareness are consistently used	Proper safety precautions and awareness are generally used	Proper safety precautions and awareness are missed

Pleistonaho with Learning Outcome (8(1) Wide knowledge on Solume and Eigheering and proceed ability to apply them to ache problems in the acids. (7(1) ABIN to operate and administrar the computer activates and hardware

Teaching Method

Outline:	Students will use command line on VM, the CuberPi robot, and digital circuit,	
Class Format:	Labwork	
Please Note :	Complying with safety rules. Lab work topics are subject to be changed due to the school schedule. All reports must be submitted and accepted to set the credit	

urse Plan Semester 2	Contents and Method of Course	Goals	Related MCC
ORINOTES Z	CONTRINE AND MISTRO OF COURSE	Understanding Guidance.	VI-D 1 S
1st week	Guidance Introduction of course Instructions for	Introduction of course.	
	Guidance, Introduction of course, Instructions for labwork / VM Installation	Introduction of course, Instructions for labwork / Understanding VM instillation	
		instilation	
			VI-D 1 10
2nd week	Command Line Operations on VM	Understanding Command Line Operations on VM	
		Line Operations on VM	
			W-D 1 1
			W-D I I
3rd week	Command Line Operations on VM	Understanding Command Line Operations on VM	
		C 10 000 000 10 01 1 111	
			W-D 1 1
		Understand - Comment	
4th week	Command Line Operations on VM	Understanding Command Line Operations on VM	
			VI-D 1 1
5th week	CyberPL and mBot2	Understanding CyberPi and mBot 2	
OUT WHEK	Cyber Pt and mbotz	mBot 2	
		Understanding CyberPi and mBot 2, and line-following robot	VI-D 1 1
6th week	CyberPt. mBot2, and line-following robot		
7th week	Midterm Preparation		
			1
	Midterm Exam	No exam	
	S.E.	01/03/2024	
	Manageratura	04/400/0004	
	Monday class	01/120/2024	
			W-D 1 1
8th week	CyberPt. mBot2, and line-following robot	Understanding CyberPi and mBot 2, and line-following robot	
		robot	
			W-D 1 1
			W-D I I
9th week	CyberPi, mBot2, and line-following robot	Understanding CyberPi and mBot 2, and line-following robot	
		ropot	
			VI-D 1 6
		Hadandardar TD and	
10th week	TTL and DTL gate (Tinker CAD)	Understanding TTL and DTL gate	
			VI-D 1 6
		Understanding TTL and	
11th week	TTL and DTL gate (Tinker CAD)	Understanding TTL and DTL gate	
			
			VI-D 1
12th week	TTL and DTL gate (Actually circuit)	Understanding TTL and DTL gate	
		DTL gate	<u> </u>
			W-D 1
13th week	TTL and DTL gate (Actually circuit)	Understanding TTL and DTL gate	
		DIL gate	
			W-D 1 8
	1	Understanding Logic gate and digital circuit with IC	vi-D 1 8
14th week	Logic Gate and Digital Circuit with IC		<u> </u>
	-	-	
15th week	Final Exam Preparation		1
	Final Exam	No exam	
		Review and aummorize	
	Reflection and review	Review and summarize learning.	
			-
			Do
	Examination Performance	Behavior	Report Portfolio Oth
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